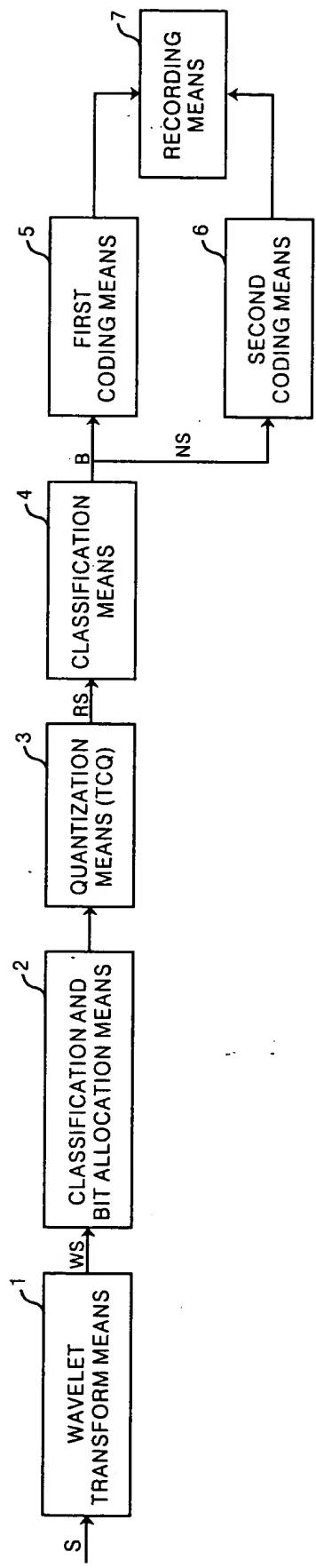


FIG. 1
SIGNAL PROCESSING SYSTEM

FIG. 1



LL1	HL0
LH0	HH0

LL2	HL1
LH1	HH1

LL3	HL2	HL1	HL0
LH2	HH2		
LH1	HH1	HL0	
LH0	HH0		

FIG.2a FIG.2b FIG.2c

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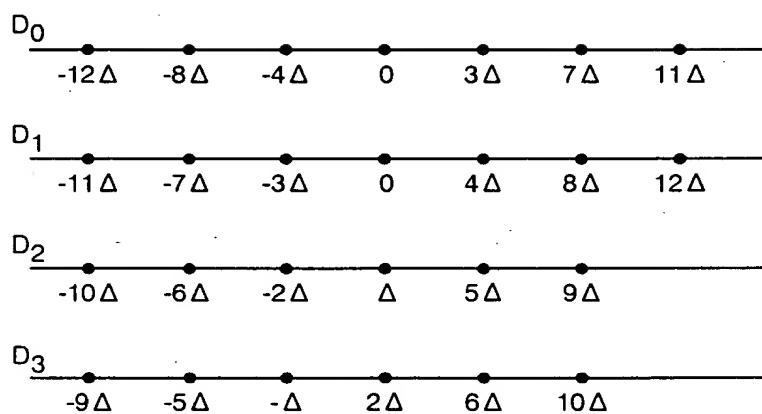


FIG.3

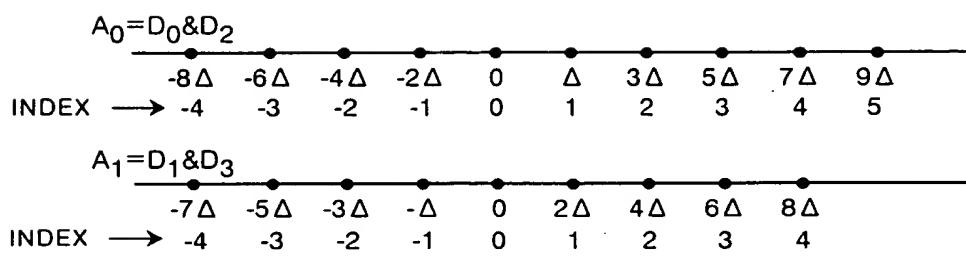


FIG.4

00 01 10 11

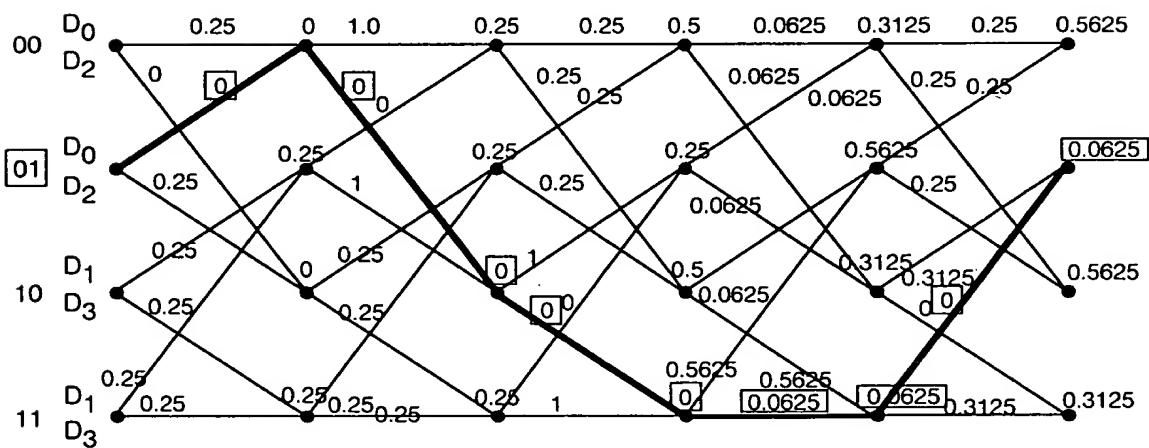


FIG.5

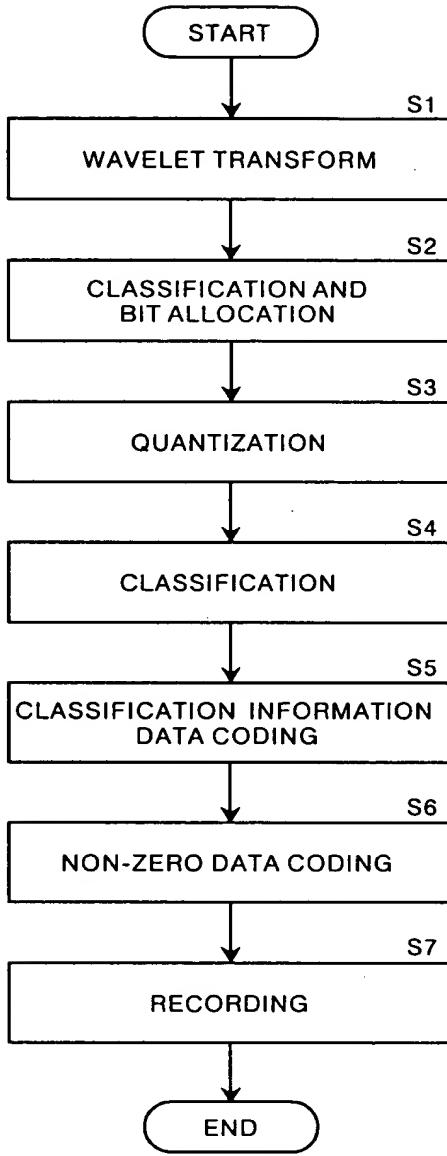


FIG.6

FIG. 7

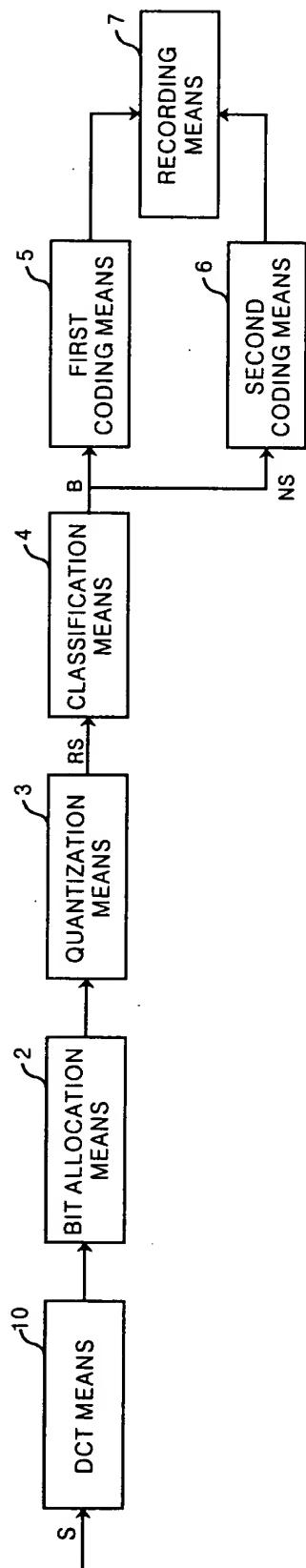
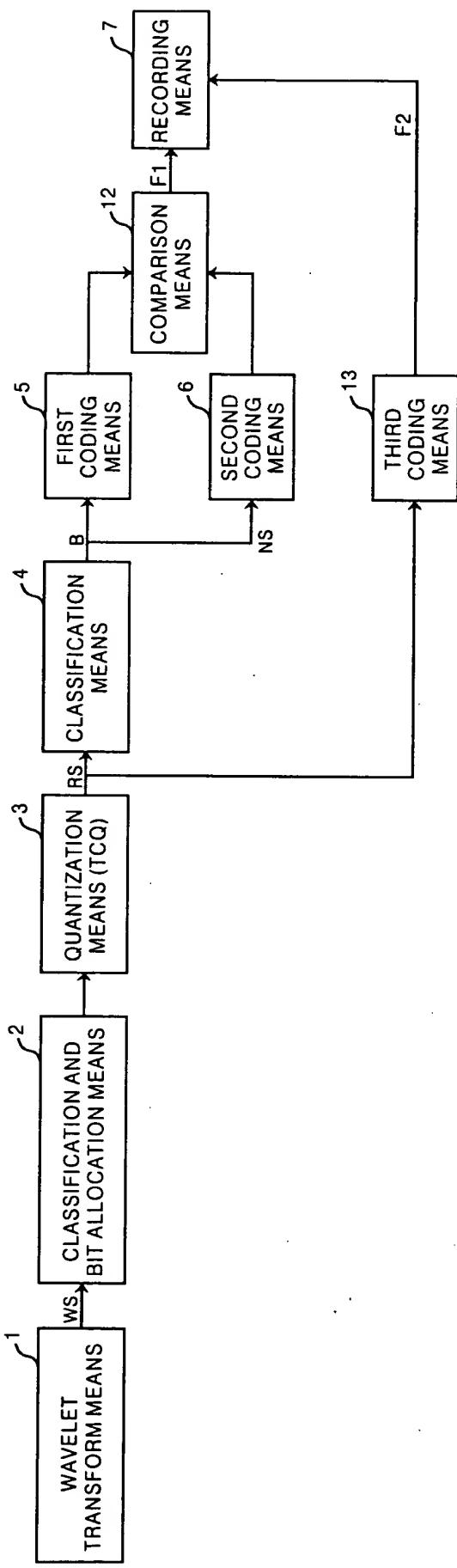


FIG. 8



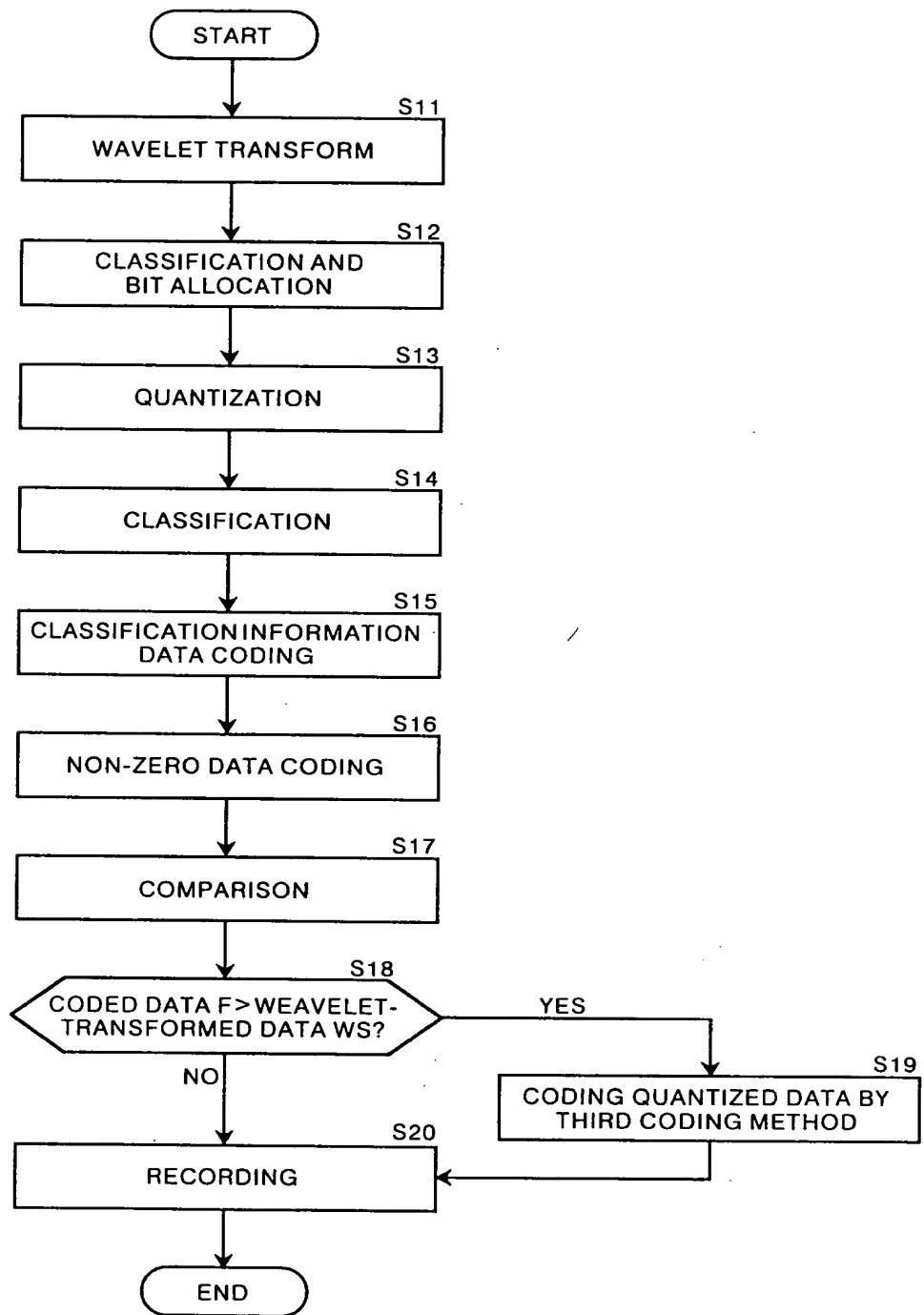


FIG.9

FIG. 10

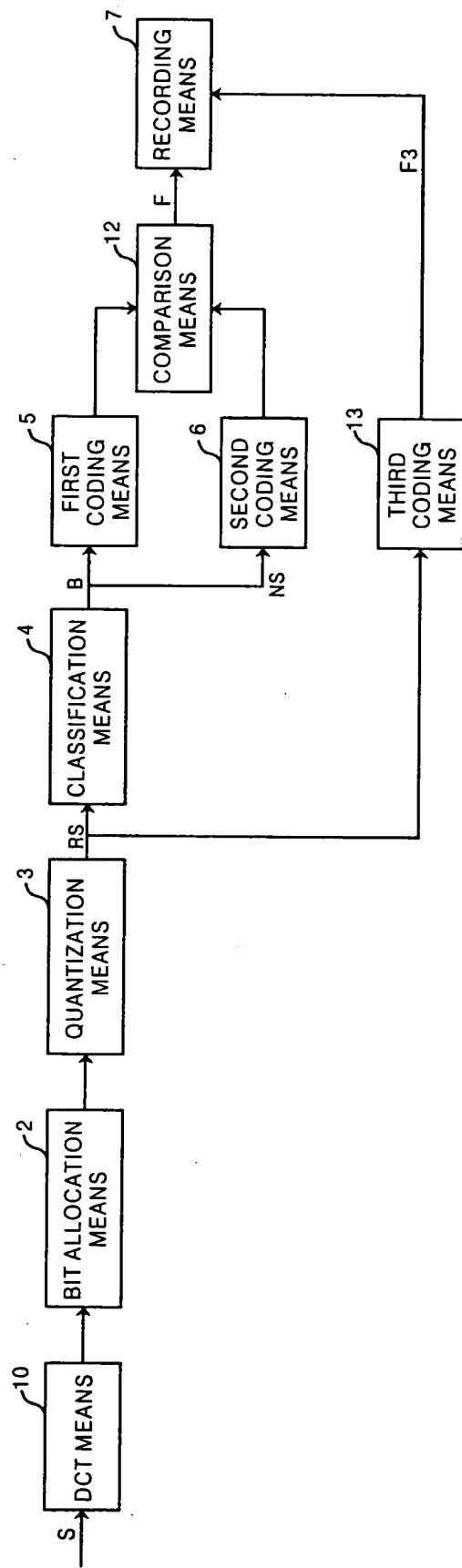


FIG. 11

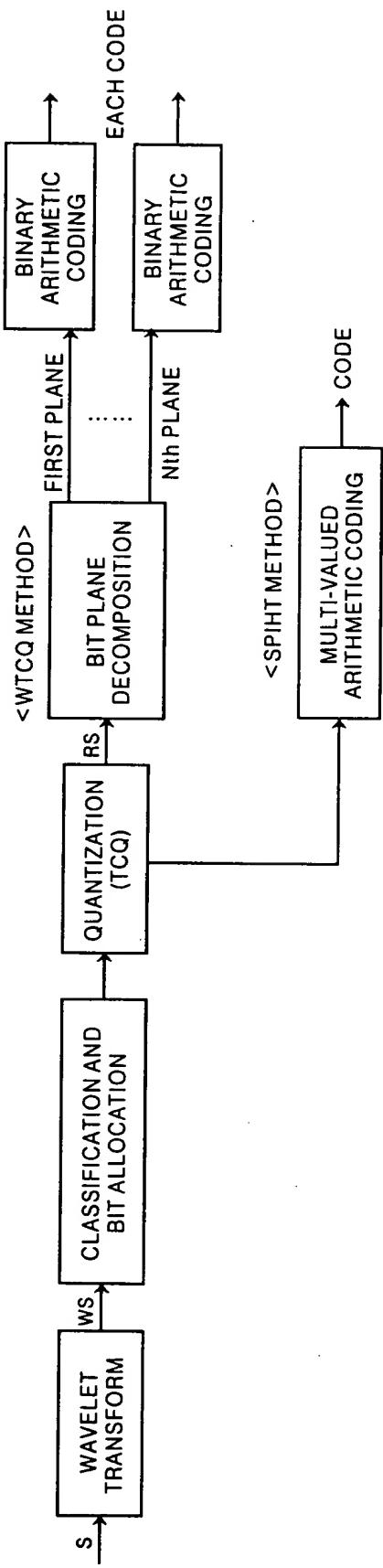


FIG. 12

